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LSI Logic Corporation
Legal Department - IP
1621 Barber Lane, MS D-106
Milpitas, CA 95035Date June 10, 2005Number of pages including cover sheet 2**To: US PTO****Mail Stop Status Request****TC 2825**Fax No. **703-872-9306**

Phone No.

CC:

From: Connie del Castillo**Intellectual Property Paralegal**Telephone No. **(408) 433-7191**Fax No. **(408) 433-7460****REMARKS:**☐ Urgent☐ For your review☒ Reply ASAP☐ Please comment

Application Number: 10/632,622
Filing date: July 31, 2003
First named inventor: Vikram Shrowty et al.
Attorney docket number: 03-0873 / 2889P

Transmitted herewith for filing via facsimile:

- Status Request Letter

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of :

Vikram Shrotry et. al.

Serial No. : 10/632,622

Filed : July 31, 2003

For : Method for Providing Clock-Net Aware Dummy Metal Using Dummy Regions

Group Art Unit : 2825

Examiner :

Atty Docket : 2889P / 03-0873

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STATUS REQUEST

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicants request a status of the above referenced application. Please mail the status notification to the address stated below.

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Date: 10 JUN 05

Respectfully submitted,

2021

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Reg. No. 36,771

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